



PATENT

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Date February 7, 2003

Denise Sheridan
Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Timothy B. Cowles et al.

Attorney Docket No.: 500792.03

Serial No. : 10/043,680

Group Art Unit : 2824

Filed : January 10, 2002

Examiner : Van Thu T. Nguyen

Confirmation No. : 9219

Date of Notice
of Allowance : January 15, 2003

Title : REFRESH CONTROLLER AND ADDRESS REMAPPING CIRCUIT AND METHOD
FOR DUAL MODE FULL/REDUCED DENSITY DRAMS

Box Issue Fee
Commissioner of Patents
Washington, DC 20231

AMENDMENT AFTER ALLOWANCE
(UNDER 37 C.F.R. § 1.312)

Sir:

Applicants acknowledge receipt of the Notice of Allowance dated January 15, 2003.

In the Specification:

Please replace the paragraph beginning at page 16, line 26, with the following rewritten paragraph:

As mentioned above, the column address buffers 34 are designed to remap one of the row address to a column address so that the SDRAM 20 is plug compatible with SDRAMs adapted to receive row addresses for M rows and column addresses for N/2 columns. With

Match and Return